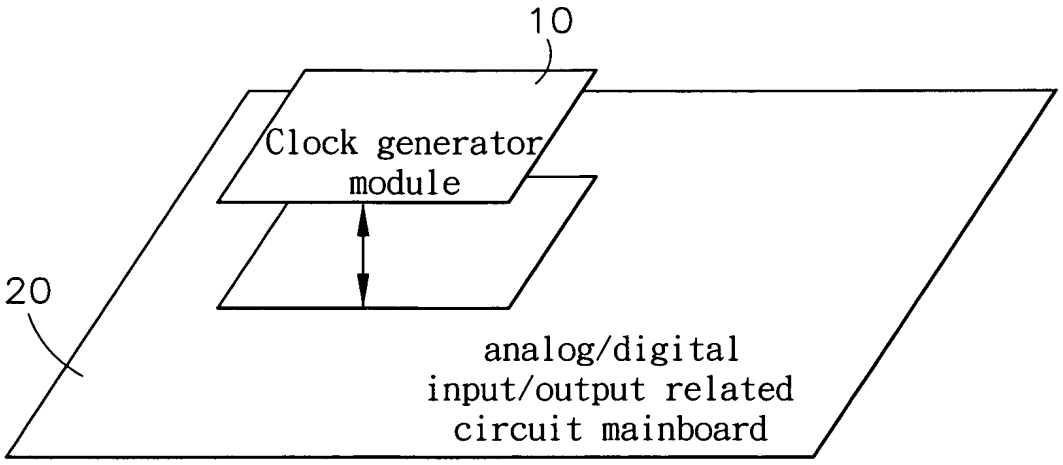
10
Clock generator module
20
analog/digital input/output related circuit mainboard

FIG. 1

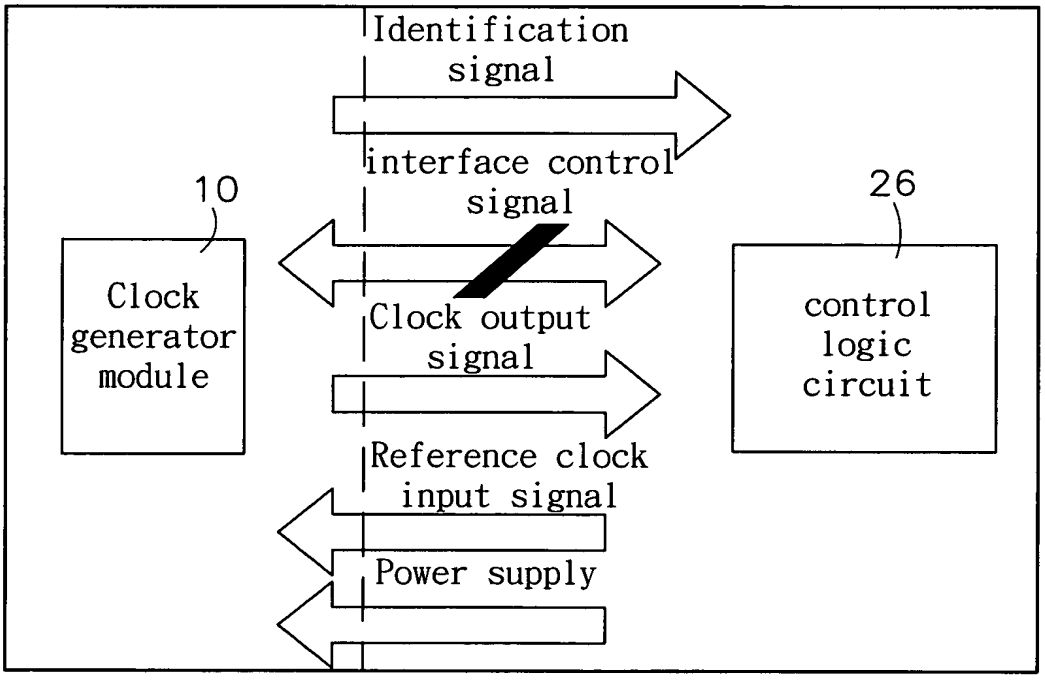
Identification signal
interface control signal
10
26
Clock generator module
Clock output signal
control logic circuit
Reference clock input signal
Power supply

FIG. 2

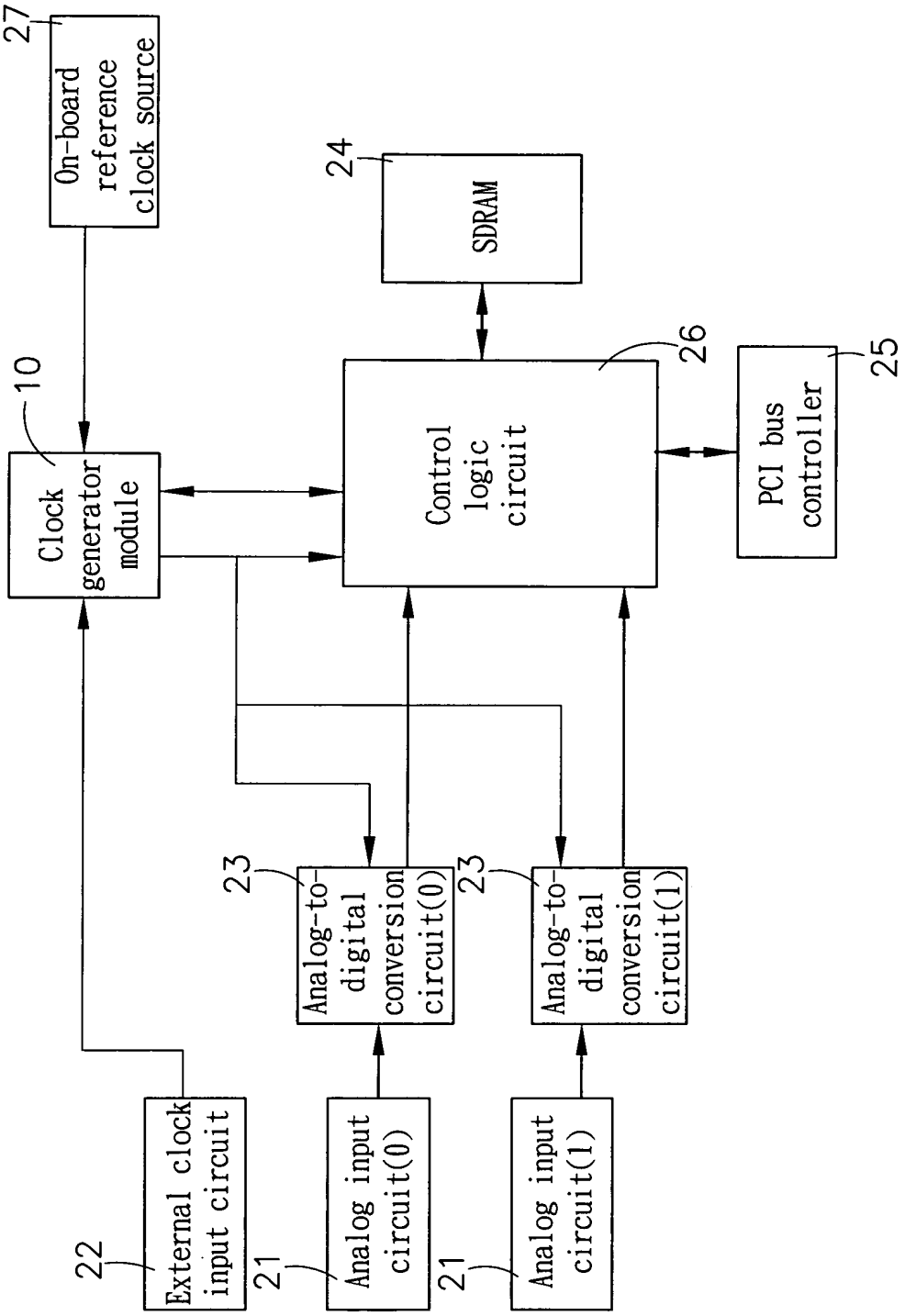
27
On-board reference clock source
24
SDRAM
10
Clock generator module
Control logic circuit
26
PCI bus controller
25
23
Analog-to-digital conversion circuit(0)
23
Analog-to-digital conversion circuit(1)
22
External clock input circuit
21
Analog input circuit(0)
21
Analog input circuit(1)

FIG.3

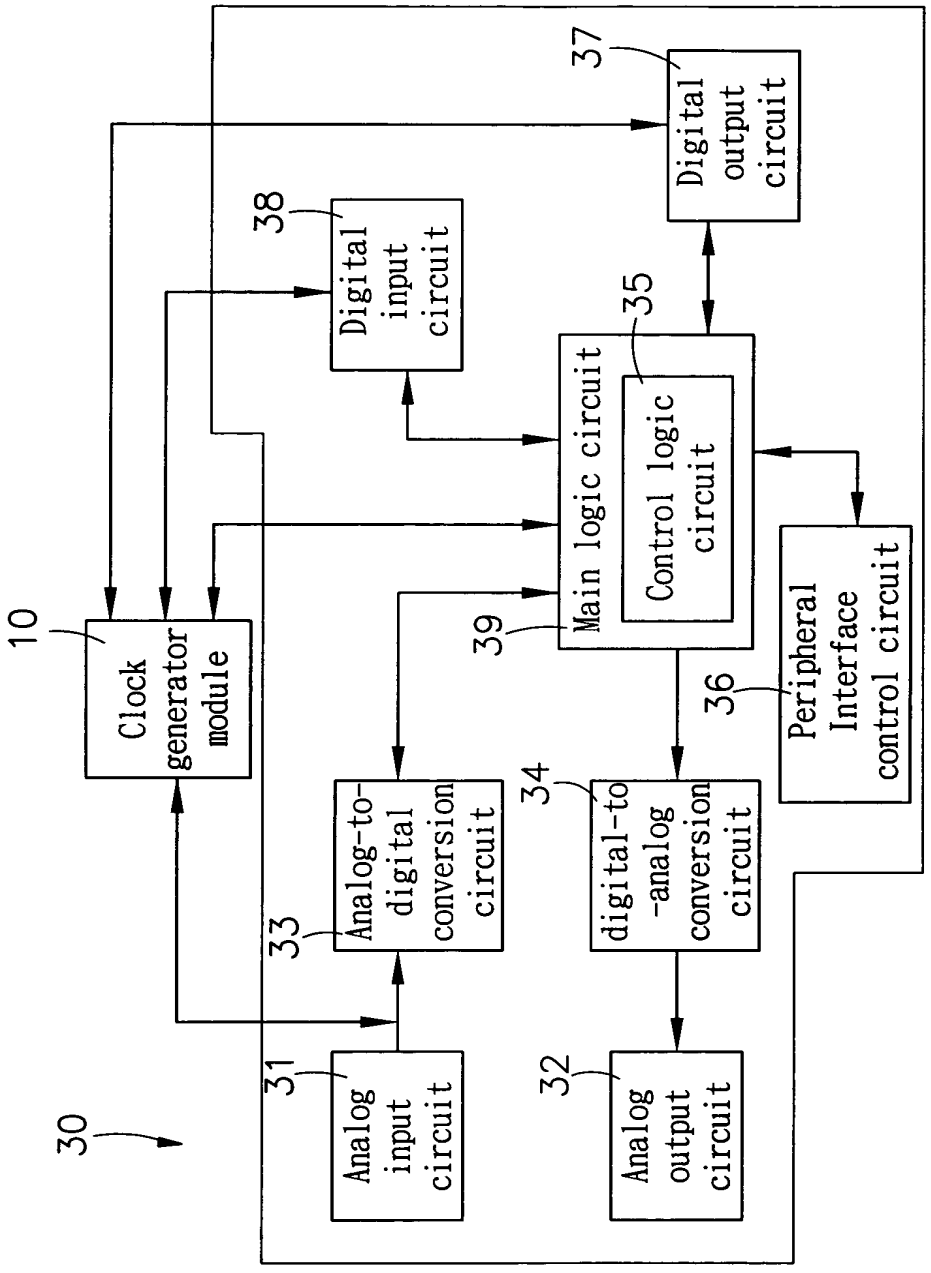
30
10
Clock generator module
31
Analog input circuit
32
Analog output circuit
33
Analog-to-digital conversion circuit
34
digital-to-analog conversion circuit
35
Main logic circuit
Control logic circuit
36
Peripheral Interface control circuit
37
Digital output circuit
38
Digital input circuit
39

FIG. 4